Search Notes ‖‖‖‖‖‖‖‖‖‖	Application/Control No. 10/092,906	Applicant(s)/Patent under Reexamination CHEN ET AL.	
	Examiner Peter Coughlan	Art Unit 2129	

SEARCHED			
Class	Subclass	Date	Examiner
706	1	3/3/2006	PDC
706	15	3/3/2006	PDC
706	45	3/3/2006	PDC
700	1	3/3/2006	PDC
700	90	3/3/2006	PDC
375	354	3/3/2006	PDC
704	256	3/3/2006	PDC
701	110	3/3/2006	PDC
713	322	3/3/2006	PDC
713	501	3/3/2006	PDC

SEARCH NOTES (INCLUDING SEARCH STRATEGY)		
	DATE	EXMR
East--neural network, frequency, rate, clock speed, chen, tseng, CPU, vector, radial, change, alter, variable, parameter	3/3/2006	PDC
East--II--register, input, clock skew, clock multiplier, ide, pci, dma, gaussian, multiquadric, agp, external frequency	3/3/2006	PDC
East--III--makagon.in., monza.in., training,	3/3/2006	PDC
Inventors I-Larn Chen Yuh-Dar Tseng	3/3/2006	PDC
IEEE--CPU, changing/altering rates - frequency - clock, tarining ide / clock multiplier / dma / pci / apg, I-Larn Chen, Tseng	3/3/2006	PDC
375.354 with multiple clocks, 704.256 with training with past and present CPU frequencies	3/3/2006	PDC
701.110 with training with DMA 713.322 with neural network and changing clock rate	**3/3/2006**	**PDC**
713.501 with clock multiplier factor	3/3/2006	PDC

INTERFERENCE SEARCHED			
Class	Subclass	Date	Examiner